

Five Days Online FDP

On

Advanced Semiconductor Material and Devices

7th – 11th Jan. 2022



Organized by

Micro and Nano devices Research Group
School of Electronics Engineering (SENSE)

Vellore Institute of Technology Chennai Campus



Chennai – 600127

About the FDP

Semiconductor technology has revolutionized human lives for the past two century. Starting from the invention of junction transistor to advanced filed effect transistors this technology is the key for developments in various fields like medical, communication and power industry. This faculty development programme is intended to address recent advancements in the field of semiconductor material and devices. This FDP covers the synthesize and processing of semiconductor material ranging from elementary Silicon, Germanium and compound semiconductor material like SiC, GaN and their challenges. Evolution and progress of semiconductor devices from conventional transistors to advanced field effect transistor and HEMT devices will also be discussed. Wide spectrum of application of semiconductor devices extending from high power electronics, high temperature sensors and Bio sensors are the major focus of this FDP. Further computational tools and their role in the development of modern semiconductor devices will also be covered in this FDP.

Coordinators

Dr. E. Papanasam

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Dr. Prashanth Kumar

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Who can Attend

Faculty members

Industry persons, Engineers from R&D organization

Research Scholars, PG students

How to Register

Interested participants apply through online registration link

<https://forms.gle/qTeJiC6fKsRZ9E5r9>

E-Certificate will be issued to all the participants

Last date for Registration: 4th Jan. 2022

About VIT

VIT for the past 36 years has made a mark in the field of higher education in India imparting quality education in a multicultural ambience, intertwined with extensive application-oriented research. VIT was established with the aim to provide quality higher education on par with international Standards.

It persistently seeks and adopts innovative methods to improve the quality of higher education on a consistent basis. VIT was established by a well-known educationalist and former parliamentarian, Dr. G. Viswanathan, Founder and Chancellor, a visionary who transformed VIT into a center of excellence in higher technical education. Govt. of India recognized VIT as an Institution of Eminence (IoE). ARIIA, Govt. of India recognized VIT as a No. 1 Private University for Innovation. MHRD, Govt. of India ranked VIT as No.15 among the Engineering Institutions (NIRF-2020 ranking). VIT Chennai is ably spearheaded by Mr. Sankar Viswanathan, Vice President, Ms. Kadhambari S Viswanathan, Assistant Vice President, Dr. Rambabu Kodali, Vice-Chancellor and Dr. V. S. Kanchana Bhaaskaran, Pro Vice-Chancellor. They share in the mission to make VIT a global leader.

ABOUT SENSE

The School of Electronic Engineering (SENSE) at VIT Chennai was established in 2010 when this campus was set up in Chennai. The School offers two B.Tech. programmes, one in Electronics and Communication Engineering and the other in Electronics and Computer Engineering. It also offers two M.Tech programmes, one in Embedded Systems and the other in VLSI Design. In addition, Ph.D. research programme is offered in the areas of Electronics, Communication and Computer Engineering and their allied fields. The School has modern state-of-the-art laboratories in the areas of Semiconductor Devices, Micro and Nano Devices, Analog Circuit Design, Digital System Design, Digital Signal Processing, Embedded Systems and Architecture, Microprocessors and Microcontrollers, Communication Engineering, Wireless Technologies, Internet of Things (IoT), Microwave and Optical Communication, Computer Networks and Advanced VLSI Design.

Experts/Resource Person

Dr. Jayeeta Bhattacharyya

Associate professor IIT Madras

Dr. Jawar Singh

Associate professor, IIT Patna

Dr. Amit Verma

Assistant Professor, IIT Kanpur

Dr. Brajesh Rawat

Assistant professor, IIT Ropar

Dr. S. Santosh Kumar

Principal Scientist, CSIR-CEERI, Pilani

Dr. Pankaj B. Agarwal

Principal Scientist, CSIR-CEERI, Pilani

Dr. C. Periasamy,

Assistant Professor, NIT Jaipur

Dr. Rajesh Saha,

Assistant professor, NIT Jaipur

Dr. Chandan Yadav

Assistant professor, NIT Calicut

Dr. Gopi Krishna Saramekala

Assistant professor, NIT Calicut

Dr. V. Muthubalan,

Professor, SASTRA Deemed University, Thanjavur

Dr. Sumit Kale,

Assistant Professor, Delhi Technical University

Dr. Rupam Goswami,

Assistant Professor, Tezpur University, Assam

Dr. Kumar Prasannajit Pradhan,

Assistant professor, IIITDM Kancheepuram

Mr. Amit Saini

Director, Cadre Design Systems



Registration
Free