



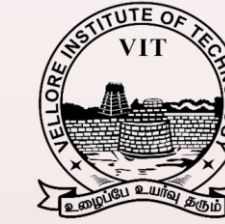
ABOUT SCHOOL OF ELECTRONICS ENGINEERING (SENSE)

The School of Electronics Engineering (SENSE) at VIT was established for imparting the state-of-the-art education, training and research in the field of Electronics & Communication Engineering and allied areas. The School offers two B.Tech. programmes, one in Electronics and Communication Engineering and the other in Electronics and Computer Engineering. It also offers two M.Tech programmes, one in Embedded Systems and the other in VLSI Design. In addition, Ph.D. research programme is offered in the areas of Electronics, Communication and Computer Engineering and their allied fields. The School has modern state-of-the-art laboratories in the areas of Semiconductor Devices, Micro and Nano Devices, Analog Circuit Design, Digital System Design, Digital Signal Processing, Embedded Systems and Architecture, Microprocessors and Microcontrollers, Communication Engineering, Wireless Technologies, Internet of Things (IoT), Microwave and Optical Communication, Computer Networks and Advanced VLSI Design.

ABOUT VIT CHENNAI

VIT for the past 36 years has made a mark in the field of higher education in India imparting quality education in a multicultural ambience, intertwined with extensive application-oriented research. VIT was established with the aim to provide quality higher education on par with international Standards. It persistently seeks and adopts innovative methods to improve the quality of higher education on a consistent basis. VIT was established by a wellknown educationalist and former parliamentarian, Dr. G. Viswanathan, Founder and Chancellor, a visionary who transformed VIT into a center of excellence in higher technical education. Govt. of India recognized VIT as an Institution of Eminence (IoE). ARIIA, Govt. of India recognized VIT as a No. 1 Private University for Innovation. MHRD, Govt. of India ranked VIT as No.15 among the Engineering Institutions (NIRF-2020 ranking). VIT Chennai is ably spearheaded by Mr. Sankar Viswanathan, Vice President, Ms. Kadhambari S Viswanathan, Assistant Vice President, Dr. Rambabu Kodali, Vice-Chancellor and Dr. V. S. Kanchana Bhaaskaran, Pro ViceChancellor. They share in the mission to make VIT a global leader. The focus is:

- To maximize the Industrial Connectivity
- To create Centre of Excellence in niche areas
- To enrich Technological and Managerial Human Capital nurtured in a multicultural ambience
- To provide common platform for the agglomeration of ideas of personnel from various walks of life for learning enrichment
- To create opportunities and exploit the available resources to benefit industry and society
- To encourage participation in the National Agenda of knowledge building
- To foster International collaborations for mutual benefits in areas of research



VIT[®]

Vellore Institute of Technology
(Deemed to be University under section 3 of UGC Act, 1956)
CHENNAI

School of Electronics Engineering

(SENSE)

Organizes

Online FDP

on

**Current Trends on Data Science
and its Applications**
(13th - 15th July 2022)

Co-ordinators

Dr. S. Shoba
Dr. A. Sasithradevi

VIT – Recognised as Institution of Eminence (IoE) by Government of India
VIT – A place to learn; A chance to grow

ABOUT THE FDP

In the current trend, institutions anticipate that intelligent automation solutions would help them to address the real world problems and boost their organization's production and efficiency. Data science has to be included into all academic programme, and academicians have to take courses in these cutting-edge technologies. Data science related technologies are now increasing market demand, need to be used in interesting ways. The potential advantages of these technologies are not well understood. This FDP offers academicians the practical knowledge and industry connections for solving real-world case studies.

WHO SHALL ATTEND?

This programme is intended for young researchers, faculty members and industry persons. The motive is to expose the participants to the recent trends in data science and its applications to solve the real world problems.

COORDINATORS

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RESOURCE PERSONS



• **Dr. Adeline Sneha**
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Research Director, Bandar Puchong Jaya, Selangor,
Malaysia



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Consultant, Tata Consultancy Services, SIPCOT,
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• **Dr. S Mohamed Mansoor Roomi**
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• **Dr. L. K. Pavithra**
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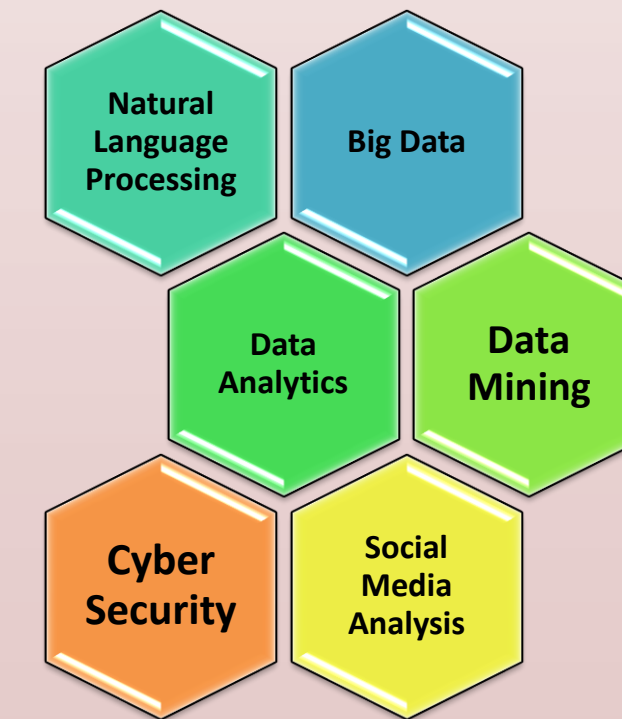


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HIGHLIGHTS OF FDP



REGISTRATION

Dates to Remember:

Registration Last Date: 11-06-2022

Confirmation: 12-06-2022

Registration Fee:

Course is offered without any fee

Course registration Link

<https://vitchennaievents.com/>

***E-Certificate will be provided to the registered participants only.**