

### ABOUT SENSE

The School of Electronics Engineering (SENSE) at VIT was established for imparting the state-of-the-art education, training and research in the field of Electronics & Communication Engineering and allied areas. It offers two B. Tech programs, one in Electronics and Communication Engineering and another in Electronics and Computer Engineering, two M. Tech programs one in VLSI Design and another in Embedded Systems, PhD in the related domains of ECE & ECM. The expertise of the faculty members includes VLSI Design, Communication Engineering, Embedded Systems, MEMS, nano- electronics and nano-technology, Photonics and Signal Processing.

### ABOUT CNVD

The Centre for Nano-electronics and VLSI Design (CNVD) was established in March 2020. The centre mainly focuses on the design, modeling and fabrication of nano-scaled devices and integrated circuits for the industrial and consumer electronics applications. The major research areas of the centre are low power digital VLSI circuits, analog integrated circuits MEMS and CMOS integration, nanoscale devices and circuits, hardware security FPGA based systems.

### ABOUT VIT CHENNAI

VIT University for the past 36 years has made a mark in the field of higher education in India imparting quality education in a multi-cultural ambience, intertwined with extensive application-oriented research. VIT University was established with the aim to provide quality higher education on par with International Standards. It persistently seeks and adopts innovative methods to improve the quality of higher education on a consistent basis.

Established by well-known educationalist and former parliamentarian, **Dr. G. Viswanathan, Founder and Chancellor**, a visionary who transformed VIT into a center of excellence in higher technical education. VIT Chennai is ably spearheaded by **Mr. Sankar Viswanathan, Vice President, Ms. Kadhambari S. Viswanathan, Assistant Vice President, Dr. Rambabu Kodali, Vice Chancellor** and **Dr. V S. Kanchana Bhaaskaran**, Pro Vice Chancellor of VIT University. They share in the mission to make VIT a global center. The focus is to:

- ▣ To maximize the Industrial Connectivity.
- ▣ To create Centers of Excellence in niche areas of research.
- ▣ To enrich Technological and Managerial Human Capital nurtured in a multicultural ambience.
- ▣ To provide common platform for the agglomeration of ideas of personnel from various walks of life for learning enrichment.
- ▣ To create opportunities and exploit the available resources to benefit industry/society.
- ▣ To encourage participation in the National Agenda of knowledge building.
- ▣ To foster International collaborations for mutual benefits in areas of research concerned.

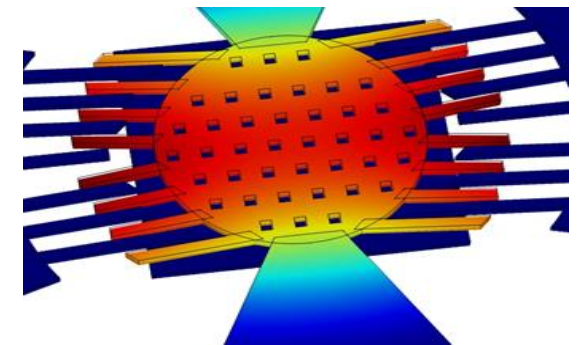


**VIT**<sup>®</sup>  
**Vellore Institute of Technology**  
(Deemed to be University under section 3 of UGC Act, 1956)  
CHENNAI

**One day International Seminar**  
**On**

**Interoperable design techniques of integrated CMOS- MEMS sensor system**

**April 9<sup>th</sup> 2021**



**CONVENERS**

**Dr. Ananiah Durai S**  
**Prof. Manikandan P**

Organized by

**School of Electronics Engineering &**  
**Centre for Nano-Electronics & VLSI Design**

**VIT Chennai**

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## ADDRESS FOR CORRESPONDENCE

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School of Electronics Engineering,  
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## REGISTRATION DETAILS

Registration Free:

G-meet Link: [meet.google.com/dpo-hvix-edx](https://meet.google.com/dpo-hvix-edx)

GFORM

<https://forms.gle/W5VntYmcSRJULZ5u8>

## DATES TO REMEMBER

Last date for registration: 5<sup>th</sup> April 2021

Confirmation to the participants: 8<sup>th</sup> April 2021

## CONDUCT OF PROGRAMME

Session will be conducted online over Google Meet platform. The detailed schedule and meeting links would be sent to individual participants by an email after the completion of registration and payment process.

## OBJECTIVE OF THE PROGRAM

The main objective of this program is to provide the details of the basic concepts in understanding the IntelliSuite and to learn how to design MEMS sensor for any specific application and provides interfacing information of sensors with other technologies.

## PROGRAM HIGHLIGHTS

This International seminar mainly focuses on giving the essence of IntelliSuite with its applications. IntelliSuite is the industry-leading tool set for MEMS layout design, advanced process simulation, FEA, parametric analysis, system simulation, packaging analysis. Export of the MEMS sensor model to cadence for integrated simulation and Verification will also be explored.

## TARGET PARTICIPANTS

Faculty, research scholars and under/post graduate students from science/engineering background can apply.

## RESOURCE PERSONS

- **Dr. Nireekshan Kumar S**  
Shelston IP BoltsnVolts  
Patent Scientist,  
Sydney, Australia.
- **Mr. Sripadaraja K**  
MEMS technical specialist  
IntelliSense Corporation,  
Bangalore.
- **Dr. Ananiah Durai S**  
Associate Professor,  
School of Electronics Engineering,  
VIT Chennai.
- **Prof. Manikandan P**  
Assistant Professor,  
School of Electronics Engineering,  
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