



**VIT**<sup>®</sup>  
**Vellore Institute of Technology**  
(Deemed to be University under section 3 of UGC Act, 1956)  
CHENNAI

## A Five-Day Online FDP

on

## “IoT and Machine Learning Applications to Engineering Systems”

21<sup>st</sup> to 25<sup>th</sup> November 2022

Organized by  
**School of Electrical Engineering**



### Coordinators

**Dr. Vaithilingam C.**

**Dr. P. Sriramalakshmi**

VIT – A place to learn; A chance to grow

### ABOUT VIT

Founded in 1984, VIT has made a mark in the field of higher education in India imparting quality education intertwined with extensive application-oriented research in a multi-cultural ambience. VIT aims to provide quality higher education in accordance with International Standards. It persistently seeks and adopts innovative methods to improve the quality of higher education on a consistent basis. VIT was established by a well-known educationalist and former parliamentarian, Dr. G. Viswanathan, the Founder and Chancellor, a visionary who transformed VIT into a Center of Excellence in higher technical education. The MHRD, Govt. of India ranked VIT the 9<sup>th</sup> Best University, the 10<sup>th</sup> Best research institution and the 12<sup>th</sup> Best Engineering Institutions in India (NIRF Ranking, Govt. of India 2022). As per QS World University Rankings by Subject 2022, Engineering and Technology subject areas of VIT are the 346<sup>th</sup> Best in the World and the 9<sup>th</sup> Best in India. VIT has also been accredited A<sup>++</sup> grade by NAAC 4<sup>th</sup> cycle.

### ABOUT VIT CHENNAI

VIT Chennai is ably spearheaded by Vice Presidents Mr. Sankar Viswanathan, Dr. Sekar Viswanathan, Mr. G. V. Selvam, Asst. Vice President Ms. Kadhambari S. Viswanathan, Vice-Chancellor Dr. Rambabu Kodali, and Pro Vice Chancellor Dr. V. S. Kanchana Bhaaskaran. They share in the mission to make VIT a global center towards academic and research excellence.

The focus is:

- Maximizing the industrial connectivity
- Creating centers of excellence in niche areas
- Enriching technological and managerial human capital nurtured in a multicultural ambience
- Providing a common platform for the agglomeration of ideas of personnel from various walks of life for learning enrichment
- Creating opportunities that exploit the available resources to benefit industry and society
- Encouraging participation in the National Agenda of knowledge building
- Fostering international collaborations for mutual benefits in areas of research

### ABOUT SCHOOL OF ELECTRICAL ENGINEERING (SELECT)

The School of Electrical Engineering (SELECT) at VIT Chennai, was established in 2010. Since the inception of the campus, the department has developed state-of-the-art infrastructure for teaching and research. Smart Grid lab at VIT is unique of its kind in the country. The school has highly qualified and experienced faculty members. The school offers B.Tech. and Ph.D. programmes in Electrical and Electronics Engineering. There are four major research groups: Power Systems, Power Electronics, Control & Instrumentation and Electrical Machines & Drives. The research findings of these groups are published in various peer-reviewed International Journals with high impact factor.

The school has always endeavored to contribute significantly to the growth of technical education by organising several workshops, seminars and guest lectures for the benefit of faculty members, students and research scholars of educational institutions in and around the region. The school has also signed MoUs with various industries for collaborative work. Eminent professors from reputed Indian and foreign universities share their expertise for the benefit of the students. VIT-Electrical and Electronics Engineering appears in the top 251-300 of the QS World University Ranking by subject and occupies 9<sup>th</sup> position in NIRF Ranking 2022.



### FACULTY COORDINATORS

**Dr. Vaithilingam C., Associate Professor**

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**Dr. P. Sriramalakshmi, Associate Professor**

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School of Electrical Engineering (SELECT)

Vellore Institute of Technology (VIT)

Vandalur- Kelambakkam Road

Chennai – 600127

**RESOURCE PERSONS:**

Day 01:  
Mr. Vivek Raja,  
VP - Product at NexStem (Founding Member)  
Guinness Record Holder,  
Topic: Image Processing Techniques

Day 02:  
Dr. R. Murugan,  
Assistant Professor  
Dept of ECE,  
National Institute of Technology, Silchar  
Topic: Computer Aided Diagnosis of Medical Images  
Using Deep Learning

Day 03:  
Dr. R. Sujatha,  
Associate Professor,  
Department of Embedded Technology,  
In-charge (Intelligent Industrial IoT and Computing  
Lab),  
School of Electronics Engineering (SENSE),  
Vellore Institute of Technology, Vellore, India  
Topic: Leveraging industrial IoT 4.0 for  
digital transformation

Day 04  
Dr. R. Maheswari,  
Associate Professor,  
School of Computer Engineering,  
Vellore Institute of Technology, Chennai  
Topic: IoT Automation through Open-Source Tools

Day 05:  
Dr. Gautham S.,  
Function Developer-Electrification Technology,  
BMS – Vitesco Technologies India Pvt Ltd  
Bangaluru, Karnataka, India  
Topic: Application of Deep Reinforcement Learning  
for Embedded System

**ONLINE MEETING**

Participants are requested to join online sessions  
through ZOOM platform during 10:00 a.m. to 11:30  
a.m. (all five days)

**WHO SHALL ATTEND?**

This FDP programme is intended for faculty  
members and research scholars. The FDP aims at  
providing insights into various applications of IoT  
and Machine Learning Techniques to Engineering  
Systems.

**REGISTRATION FEE**

Registration Fee: Rs. 250/-  
Payment Link: <https://vitchennaievents.com/>  
Participants are requested to register and pay the  
fee using the above link and subsequently, fill in the  
registration form.

Registration form link: <https://tinyurl.com/mu56zr6n>

**Last date of registration: 18/11/2022**  
E-Certificate will be provided to active participants  
after successful completion of the FDP.

**Registration Form**

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**on**

**“IoT and Machine Learning Applications to  
Engineering Systems”**

**21<sup>st</sup> to 25<sup>th</sup> November 2022**

Name: .....

Designation: .....

Organization: .....

Address for Correspondence:.....

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.....

Phone:.....

E-mail:.....

**Payment Details**

Amount: .....

Transaction number: .....

Date of Transaction: .....

Name of Bank:  
.....

.....

Date: Signature

**Head of the Department**