Eligible Research Guide's Details

CENTRE FOR NANO ELECTRONICS AND VLSI DESIGN (CNVD)

Employee id	Name of the Faculty	Designation	Email id	No. of vacancy	Area of Interest
50142	Dr. Sasipriya P	Professor Grade 1	sasipriya.p@vit.ac.in	3	Low Power VLSI Circuit design, Adiabtic logic circuits, Sub / Near Threshold circuit design, In Memory Computing and High Performance Architectures using Approximate Computing
50274	Dr. Anita Angeline A	Professor Grade 1	anitaangeline.a@vit.ac.in	3	High Performance IC Design, Neuromorphic Architectures
50310	Dr. Prathiba A	Associate Professor Senior	prathiba.a@vit.ac.in	4	VLSI ARCHITECTURES,DEEP AND MACHINE LEARNING,HARDWARE SECURITY
50369	Dr. Lakshmi B	Professor Grade 2	lakshmi.b@vit.ac.in	5	VLSI & Nanoelectronics
50428	Dr. Umadevi S	Associate Professor Grade 2	umadevi.s@vit.ac.in	5	Digital Filter for hearing aid applications, Delay modelling, Scripting languages usage VLSI applications, Layout design and verification

50617	Dr. Ananiah Durai S	Professor Grade 1	ananiahdurai.s@vit.ac.in	2	Analog IC Design, CMOS-MEMS
50729	Dr. Bindu B	Professor Grade 2	bindu.boby@vit.ac.in	3	Nanoelectronics, VLSI Design, Neuromorphic Computing
52321	Dr. Biswajit Jena	Associate Professor Grade 1	biswajit.jena@vit.ac.in	6	VLSI,Modeling and simulation of nanoscale devices, SOI FETs, FinFETs, Graphene FETs, NC FETs, Solar Cells, Reliability of Devices, Neuromorphic Computing, and Machine Learning in TCAD.