

Organizing committee

Chief Patron

Dr. G. Viswanathan, the Founder and Chancellor

Patrons

Mr. Sankar Viswanathan, Vice President

Dr. Sekar Viswanathan, Vice President

Mr. G.V. Selvam, Vice President

Ms. Kadhambari S Viswanathan, Assistant Vice President

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Dr. Rambabu Kodali, Vice Chancellor

Dr. V. S. Kanchana Bhaaskaran, ProVice chancellor

Convener

Dr. A. Peer Fathima, Prof. and Dean/SELECT

Organizers

Dr. L. PREMALATHA, Professor

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Dr. D. R. BINU BEN JOSE, Associate Professor

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DATES TO REMEMBER

Last date for Registration: 20.03.2022

Intimation of acceptance: 25.03.2022

Confirmation by participants: 30.03.2022

- Seats limited to 50 only

VIT - CHENNAI CAMPUS

Being founded in 1984, Vellore Institute of Technology (VIT) has emerged as one of the best institutes of India and is aspiring to become a global leader. VIT was established by the well-known educationalist and former parliamentarian, Dr. G. Viswanathan, the Founder and Chancellor, a visionary who transformed VIT into a centre of excellence in higher technical education. Quality in teaching-learning, research and innovation makes VIT unique. VIT attracts students from all the 29 states of India and more than 41 different countries because of its academic excellence. VIT has the record of publishing maximum number of SCOPUS Indexed Research Journal papers, among Indian Universities, overtaking all the premier institutions. VIT has also completed 3 cycles of NAAC accreditation and has been rated as an "A ++" grade Institution. In addition, VIT is also ranked by QS World University Ranking, Times Higher Education (THE) World University Ranking, QS and THE Asia Ranking, QS and THE BRICS Ranking, THE Young University Ranking of the world universities and others. 10 engineering programs of Vellore and 4 engineering programs of Chennai campus are accredited by ABET, USA. VIT is the first institute of India to receive QS 4-Star rating in overall category and QS 5-Star rating in teaching, employability, facilities, innovation and inclusiveness. VIT Chennai is ably spearheaded by Vice Presidents Mr. Sankar Viswanathan, Dr. Sekar Viswanathan and Mr. G.V. Selvam, Vice Chancellor Dr. Rambabu Kodali, and Pro Vice Chancellor Dr. V.S. Kanchana Bhaaskaran. They share in the mission to make VIT a global educational center.

VIT- A place to learn; A chance to grow



FIVE DAYS DST Sponsored Online STTP

on

**ELECTRIC VEHICLE
INTEGRATION INTO MODERN
POWER NETWORKS -
CHALLENGES AND SOLUTION
APPROACHES**

**4th April 2022 to 08th April
2022**

Coordinators

Dr. L. PREMALATHA

Dr. D. R. BINU BEN JOSE

Sponsored by

DST - SERB

NEW DELHI

Organized by

School of Electrical Engineering

Vellore Institute of Technology

Chennai - 600 127



VIT-Recognised as Institution of Eminence (IoE) by Government of India

About SELECT

The School of Electrical Engineering (SELECT) at VIT was established for imparting a state-of the-art education, training and research in the field of Electrical and Electronics Engineering and allied areas. It offers B.Tech., M.Tech., M.S (Research) and Ph.D. Programme in the domain of Electrical and Electronics Engineering. Electrical and Electronics Engineering has been listed in the top 451- 500 of the QS World University Ranking by Subject. The expertise of the faculty members includes Power Electronics, Electrical Drives, Power Systems, Applied Electronics, Control and Instrumentation, VLSI and other frontier areas.

About the STTP

Nowadays, modern power networks have to face a number of challenges such as growing electricity demand, aging utility infrastructure and the environmental impact of the greenhouse gases produced by conventional electric generation. Electric vehicles are emerging as one of the prominent

solutions to face the sustainability issues, like increasing the energy security. This STTP aims to address the opportunities, challenges and solution approaches to extract maximum benefits from integration of EVs to the grid and to minimize their associated impact on the distribution network.

STTP Highlights

- EV technology, opportunities and challenges.
•Design, modelling, simulation and testing of EV.
•Power electronic interfaces and battery technologies for EV
•Impact on grid by EV-PV integration
•Research potentials

Resource Person

The course contents will be delivered by experts from industries and reputed academic institutions.

REGISTRATION DETAILS (includes GST)

- Faculty members: Rs.500/-
Participants from Industry: Rs.1000/-
Research scholars/ PG scholars: Rs.300/-

Interested participants are requested to fill the registration form and upload the same in the google form (link given below) along with the details of registration fee payment.

Registration form: https://forms.gle/33ESqpia8QEcJqnA7
Registration fee link: https://vit Chennai events.com/
Click fee payment link, select STTP under EVENTS, choose the category (scholar, faculty, industry), click "ONLINE", create new account and then proceed with the payment

Five days DST sponsored Online STTP on

ELECTRIC VEHICLE INTEGRATION INTO MODERN POWER NETWORKS - CHALLENGES AND SOLUTION APPROACHES

04/04/2022 to 08/04/2022

Registration Form

Name:
Designation:
Educational qualification:
Experience:
Area of specialization/Research:.....
Organization:
Address for Correspondence:
PIN Code:
Mobile:
E-mail:

Declaration

The information provided by me is true to the best of my knowledge. I agree to abide by the rules and regulations governing the STTP under DST. If selected, I shall attend the STTP for the entire duration. I also undertake the responsibility to inform the coordinator in advance in case of inability to attend the programme.

Signature of the applicant:
Date: Place: