

## VIT Chennai

VIT for the past 35 years has made a mark in the field of higher education in India imparting quality education in a multi-cultural ambience, intertwined with extensive application-oriented research. VIT was established with the aim to provide quality higher education on par with International Standards. It persistently seeks and adopts innovative methods to improve the quality of higher education on a consistent basis. The MHRD, Govt of India ranked VIT among the top Private Engineering Institution (NIRF-2016, 2017, 2018, 2019 and 2020 ranking) established by well-known educationalist and former parliamentarian, Dr. G. Viswanathan, Founder and Chancellor, a visionary who transformed VIT into a center of excellence in higher technical education. VIT Chennai is ably spearheaded by Dr. Sekar Viswanathan, Vice President, Dr. Sandhya Pentareddy, Executive Director, Dr. Anand A. Samuel, Vice Chancellor and Dr. V S Kanchana Bhaaskaran, Pro Vice Chancellor, VIT Chennai.

## About SELECT

The School of Electrical Engineering at VIT Chennai was established in 2010 for imparting a state-of-the-art education, training and research in the field of Electrical and Electronics Engineering and allied areas. It offers B.Tech and Ph.D. Programs in the domain of Electrical and Electronics Engineering. The expertise of the faculty members includes Power Systems, Power Electronics and Drives, Applied Electronics, Control and Instrumentation, VLSI and other frontier areas. The QS World University Subject Rank for Electrical and Electronics Engineering ranges between 301-350.

## About OPAL-RT Technologies

Since 1997, industries including automotive, aerospace, power electronics and power generation have increasingly turned to OPAL-RT, transforming the company into a world leader in real-time simulation and Hardware-in-the-Loop (HIL) testing equipment for electrical, electro-mechanical and power electronics systems. OPAL-RT empowers engineers and researchers with accessible, cutting-edge, real-time simulation technology to accelerate the development of better products and more reliable energy transmission.

## About FDP

The purpose of conducting this Faculty Development Programme is to expose faculties, researchers and industry persons to real time Hardware-In-the-Loop (HIL) simulation environment for rapid prototyping of electrical systems. Hardware-in-the-Loop (HIL) simulation is a technique that is used for testing control system of any plant or process. HIL simulation to test a control system of plant or process is called HIL testing. This HIL testing the plant is replaced by a simulation of the plant (which we call the HIL simulator). The HIL simulation can greatly enhance the safe operation of plant or process.

**Day 1:** Introduction to Real time simulation discussion about various user cases.

**Day 2:** Virtual hand-on consisting of IO's demonstration.

**Day 3:** Opal-RT solution for micro-grid and smart-grid, consisting of live demo and discussion.

**Day 4:** Real time Simulation as a key technology Enabler for power system design, test and validation.

**Day 5:** Sincal software level simulation using siemens software.

**Intended Audiences:** Faculties, Research Scholars and Industry people.

**Resource Persons from OPAL – RT Technologies**

**Webinar will be on GoToWebinar Platform.**

**Register for FREE before 1st August 2020 5pm:**

<https://register.gotowebinar.com/register/8736880331094540560>

**FDP Timings: 10:30 AM to 12:30 PM**

**E-Certificate will be issued to participants who attend all five days of sessions.**

**FDP Organizers:**

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# VIT<sup>®</sup>

Vellore Institute of Technology

(Deemed to be University under section 3 of UGC Act, 1956)

**International Online  
5-Days Faculty Development Program  
On**

**Real-Time Applications of  
Opal-RT for Power  
Electronics, Smart Grid and  
Microgrids**

**3<sup>rd</sup> to 7<sup>th</sup> August 2020**

***No Registration Fee***

***Organized by***  
**School of Electrical Engineering**  
**VIT Chennai**

***In association with***

