

## ABOUT SENSE

The School of Electronics Engineering (SENSE) at VIT was established for imparting the state-of-the-art education, training and research in the field of Electronics & Communication Engineering and allied areas. It offers two B. Tech programs, one in Electronics and Communication Engineering and another in Electronics and Computer Engineering, two M. Tech programs one in VLSI Design and another in Embedded Systems, PhD in the related domains of ECE & ECM. The expertise of the faculty members includes VLSI Design, Communication Engineering, Embedded Systems, MEMS, nano-electronics and nano-technology, Photonics and Signal Processing.

## ABOUT CNVD

The Centre for Nano-electronics and VLSI Design (CNVD) was established in March 2020. The centre mainly focuses on the design, modeling and fabrication of nano-scaled devices and integrated circuits for the industrial and consumer electronics applications. The major research areas of the centre are low power digital VLSI circuits, analog integrated circuits MEMS and CMOS integration, nanoscale devices and circuits, hardware security FPGA based systems.

## ABOUT VIT CHENNAI

VIT University for the past 36 years has made a mark in the field of higher education in India imparting quality education in a multi-cultural ambience, intertwined with extensive application-oriented research. VIT University was established with the aim to provide quality higher education on par with International Standards. It persistently seeks and adopts innovative methods to improve the quality of higher education on a consistent basis.

Established by well-known educationalist and former parliamentarian, **Dr. G. Viswanathan, Founder and Chancellor**, a visionary who transformed VIT into a center of excellence in higher technical education. VIT Chennai is ably spearheaded by **Mr. Sankar Viswanathan, Vice President, Ms. Kadhambari S. Viswanathan, Assistant Vice President, Dr. Rambabu Kodali, Vice Chancellor** and **Dr. V S. Kanchana Bhaaskaran**, Pro Vice Chancellor of VIT University. They share in the mission to make VIT a global center. The focus is to:

- To maximize the Industrial Connectivity
- To create Centers of Excellence in niche areas of research
- To enrich Technological and Managerial Human Capital nurtured in a multicultural ambience
- To provide common platform for the agglomeration of ideas of personnel from various walks of life for learning enrichment
- To create opportunities and exploit the available resources to benefit industry/society
- To encourage participation in the National Agenda of knowledge building
- To foster International collaborations for mutual benefits in areas of research concerned

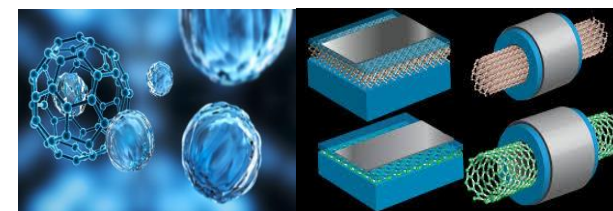


**VIT**  
Vellore Institute of Technology  
(Deemed to be University under section 3 of UGC Act, 1956)

*A Two Day Workshop on*

## **Nano Materials & Devices: Physics and Applications**

**March 11-12, 2021**



**CONVENERS**

**Dr. Arivarasi A**

**Dr. Lakshmi B**

Organized by

**School of Electronics Engineering &  
Centre for Nano-Electronics & VLSI Design,  
VIT Chennai**

Vandalur – Kelambakkam Road,

Chennai 600127

[www.chennai.vit.ac.in](http://www.chennai.vit.ac.in)

## ADDRESS FOR CORRESPONDENCE

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Assistant Professor (SG-I)  
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## REGISTRATION DETAILS

Registration Fee: Rs. 200 (GST included)

Registration link:

<https://vitchennaievents.com/>

E-Certificate will be provided to the registered participants only.

## DATES TO REMEMBER

Last date for registration: 9<sup>th</sup> March 2021

Confirmation to the participants : 10<sup>th</sup> March 2021

## REGISTRATION & CONDUCT OF PROGRAMME

Session will be conducted online over ZOOM platform. The detailed schedule and meeting links would be sent to individual participants by an email after the completion of registration and payment process

## OBJECTIVE OF THE PROGRAM

The main objective of this program is to provide the details of the basic concepts in understanding the state-of-the-art in material and device physics, characterization and reliability challenges of micro and nano devices design along with its related applications.

## PROGRAM HIGHLIGHTS

This FDP mainly focuses on giving the essence of semiconductor devices with its applications in quantum technology, graphene based hybrid nanostructures/thin films for optoelectronics, applications of finite element method with degradation physics and biomedical based nano devices used for cellular therapy/diagnostics.

## TARGET PARTICIPANTS

Faculty, research scholars and under/post graduate students from science/engineering background can apply

## RESOURCE PERSONS

- **Dr Cher Ming Tan**  
Professor and Director of Centre of Reliability Science and Technology.  
Chang Gung University, Taiwan  
IEEE Distinguished Lecturer in ED Society  
Founding Chair of IEEE Nanotechnology Chapter, Singapore  
Editor of Scientific Report, Nature Publishing  
Editor of IEEE Transaction on Device and Material Reliability  
Fellow of Institute of Engineers, Singapore  
Fellow of Singapore Quality Institute
- **Dr Tuhin Subhra Santra**  
Asst Prof, Dept. of Engg Design,  
IIT Madras, Chennai  
Wellcome Trust/DBT Indian Alliance Fellow  
iNEMS, NTHU, Taiwan

- **Dr Pradeep Kumar**  
Researcher, Nanotechnology Research Laboratory  
Center of Nanostructures and Nanodevices  
Universiti Teknologi PETRONAS,  
Perak, Malaysia
- **Dr D. Nirmal**  
Associate Professor & Head,  
Electronics & Communication Engg,  
Karunya Institute of Technology and Sciences, Coimbatore

